EXHIBIT 4











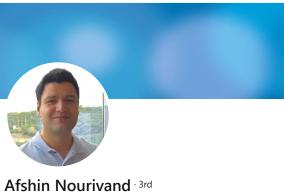












SoC Design Verification at Apple



Apple



• Concordia University

Austin, Texas Metropolitan Area \cdot Contact info

348 connections

Message



More

About

8 years of experience in the field of ASIC/FPGA/SoC design, synthesis and verification. Background in video codec standards such as HEVC and Vp9.

Background in memory interfaces such as DDR3, LPDDR, SPI.

...see more

Activity

355 followers

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Experience



SoC Design Verification

Apple

Sep 2016 - Present \cdot 8 yrs 3 mos Austin, Texas Area



Graphics Hardware Engineer

Intel Corporation

Oct 2013 - Aug 2016 · 2 yrs 11 mos Sacramento, California Area

Design and verification of video codec standards HEVC, VP9 etc. Entropy coders CABAC, CAVLC... ...see more

IBM

Advisory Engineer

IBM

Nov 2010 - Oct 2013 · 3 yrs

Exploratory Nonvolatile Memory Design







Concordia University

Ph.D., Electrical and Computer Engineering



University of Tehran

MS, Electrical and Computer Engineering

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Skills

ASIC



Endorsed by Asif Ahmed who is highly skilled at this



© Endorsed by 10 colleagues at Concordia University



29 endorsements

RTL design



60 Endorsed by 2 colleagues at Concordia University



8 endorsements

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People you may know

From Afshin's school



Naim Khan

Patent Portfolio Architect!

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Violaine Brunet Brodier

Intellectual Property Attorney (Trademark, Copyright and Patent) - Avocate en Propriété Intellectuelle (Marques de Commerce, Droit d'Auteur et...

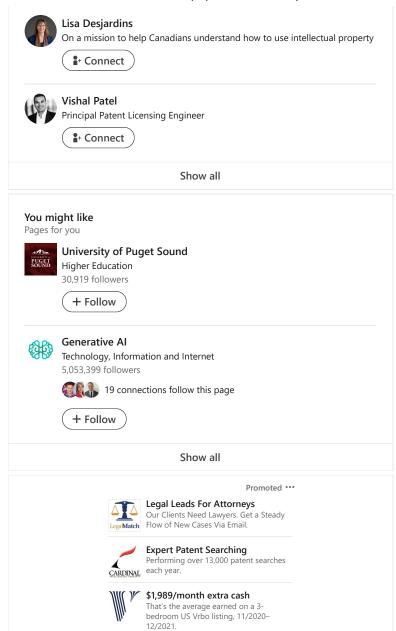
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Pierre-Olivier Savoie in

Partner chez Savoie Arbitration

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